



Arm Design Checklists User Guide

Version 1.0

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Issue 09

110244_0100_09_en



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Release information

Document history

Issue	Date	Confidentiality	Change
0100-09	8 October 2025	Non-Confidential	Updated product list (CA76, N2, GIC-600AE)
0100-08	15 September 2025	Non-Confidential	Change formatting of the note in the introduction
0100-07	21 August 2025	Non-Confidential	Confidential Doc clarification + links to DCs
0100-06	7 August 2025	Non-Confidential	Updated products list
0100-05	30 June 2025	Non-Confidential	Updated products list
0100-04	30 May 2025	Non-Confidential	Updated products list
0100-03	28 April 2025	Non-Confidential	Updated products list
0100-02	8 April 2025	Non-Confidential	Updated products list
0100-01	23 January 2025	Non-Confidential	Initial release

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PRE-1121-V1.0

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Product Status

The information in this document is Final, that is for a developed product.

Feedback

Arm welcomes feedback on this product and its documentation. To provide feedback on the product, create a ticket on <https://support.developer.arm.com>

To provide feedback on the document, fill the following survey: <https://developer.arm.com/documentation-feedback-survey>.

Inclusive language commitment

Arm values inclusive communities. Arm recognizes that we and our industry have used language that can be offensive. Arm strives to lead the industry and create change.

We believe that this document contains no offensive language. To report offensive language in this document, email terms@arm.com.

Contents

1. Introduction.....6

1.1 Conventions.....6

1.2 Other information.....7

2. Arm Design Checklists.....8

1. Introduction

1.1 Conventions

The following subsections describe conventions used in Arm documents.

Glossary

The Arm Glossary is a list of terms used in Arm documentation, together with definitions for those terms. The Arm Glossary does not contain terms that are industry standard unless the Arm meaning differs from the generally accepted meaning.

See the Arm Glossary for more information: developer.arm.com/glossary.

Typographic conventions

Arm documentation uses typographical conventions to convey specific meaning.

Convention	Use
italic	Citations.
bold	Interface elements, such as menu names. Terms in descriptive lists, where appropriate.
monospace	Text that you can enter at the keyboard, such as commands, file and program names, and source code.
monospace <u>underline</u>	A permitted abbreviation for a command or option. You can enter the underlined text instead of the full command or option name.
<and>	Encloses replaceable terms for assembler syntax where they appear in code or code fragments. For example: <pre>MRC p15, 0, <Rd>, <CRn>, <CRm>, <Opcode_2></pre>
SMALL CAPITALS	Terms that have specific technical meanings as defined in the <i>Arm® Glossary</i> . For example, IMPLEMENTATION DEFINED , IMPLEMENTATION SPECIFIC , UNKNOWN , and UNPREDICTABLE .



Caution

We recommend the following. If you do not follow these recommendations your system might not work.



Your system requires the following. If you do not follow these requirements your system will not work.



You are at risk of causing permanent damage to your system or your equipment, or harming yourself.



This information is important and needs your attention.



A useful tip that might make it easier, better or faster to perform a task.



A reminder of something important that relates to the information you are reading.

1.2 Other information

See the Arm website for other relevant information.

- [Arm® Developer](#).
- [Arm® Documentation](#).
- [Technical Support](#).
- [Arm® Glossary](#).

2. Arm Design Checklists

Design Checklists are created by Arm experts to help hardware designers check that their Arm-based designs are fit for purpose and follow Arm's recommended design guidelines.

Arm Design Checklists are a quick and easy way for engineers to verify critical aspects of their design to help prevent errors, increasing confidence in their design's success. Design Checklists can be used on their own or in conjunction with Arm Design Reviews.

Who can access Arm Design Checklists?

All partners who have a valid license for the IP covered by the checklist can access the Arm Design Checklists.



Note

Arm Design Checklists are Confidential Documents, so you must be logged in with your Arm ID to find and access them.

Intended use

Arm Design Checklists are intended to be used alongside Arm documentation to increase confidence of design success.

Some of the checks provide links to documentation on the [Arm Developer](#) website for more detailed information. Checklists must always be used alongside Arm's technical documentation.

How to access Arm Design Checklists

Users with a valid license for the relevant IP can access the Design Checklist on [Arm IP Explorer](#), [Arm Documentation Hub](#), or [Product Download Hub](#).

There are two types of Design Checklist:

- Unconfigured

The unconfigured version of the Design Checklist shows checks for every possible configuration, using IF and ELSE statements to describe the different possibilities. Unconfigured checklists are available for download from both [Arm IP Explorer](#), [Arm Documentation Hub](#), and [Product Download Hub](#).

- Configured

The configured version of the Design Checklist only shows checks for the selected configuration. You can select either the default configuration for that specific IP on Arm IP Explorer, or your own customized configuration. Note that the configured version of the checklist does not contain any IF or ELSE statements because the platform generates the checklist specifically for the selected configuration. Configured checklists are only available for download from [Arm IP Explorer](#).

For more information, watch the [Arm Design Checklists introduction video on YouTube](#).

Arm Design Checklist availability

The following Arm Design Checklists are available:

- Processors
 - Cortex-A processors
 - [Cortex-A7](#)
 - [Cortex-A32](#)
 - [Cortex-A34](#)
 - [Cortex-A35](#)
 - [Cortex-A53](#)
 - [Cortex-A55](#)
 - [Cortex-A65](#)
 - [Cortex-A65AE](#)
 - [Cortex-A72](#)
 - [Cortex-A73](#)
 - [Cortex-A76](#)
 - [Cortex-A78AE](#)
 - [Cortex-A510](#)
 - [Cortex-A710](#)
 - Cortex-R processors
 - [Cortex-R5](#)
 - [Cortex-R8](#)
 - [Cortex-R52](#)
 - [Cortex-R52+](#)
 - [Cortex-R82](#)
 - Cortex-M processors
 - [Cortex-M0](#)
 - [Cortex-M0+](#)
 - [Cortex-M3](#)
 - [Cortex-M4](#)
 - [Cortex-M7](#)
 - [Cortex-M23](#)
 - [Cortex-M33](#)
 - [Cortex-M52](#)
 - [Cortex-M55](#)
 - [Cortex-M85](#)

- Neoverse
 - [Neoverse N2](#)
- Graphics Processors
 - [Mali-G78AE](#)
- Subsystems and other IP
 - System IP
 - [CoreLink DMA-330](#)
 - [CoreLink GIC-400](#)
 - [CoreLink GIC-500](#)
 - [CoreLink GIC-600](#)
 - [CoreLink GIC-600AE](#)
 - [CoreLink GIC-700](#)
 - [CoreLink MMU-600](#)
 - [CoreLink MMU-600AE](#)
 - [CoreLink MMU-700](#)
 - [CoreSight SoC-400](#)
 - [CoreSight SoC-600](#)
 - Interconnects
 - [CoreLink CI-700](#)
 - [CoreLink CMN-600AE](#)
 - [CoreLink NI-700](#)
 - [CoreLink NIC-400](#)
 - [CoreLink NIC-450](#)
 - [CoreLink TZC-400](#)
 - [Neoverse CMN-700](#)
 - Subsystems
 - [Corstone-300](#)
 - [Corstone-310](#)
 - [Corstone-1000](#)

For more information, visit [Design Checklists and Design Reviews](#).